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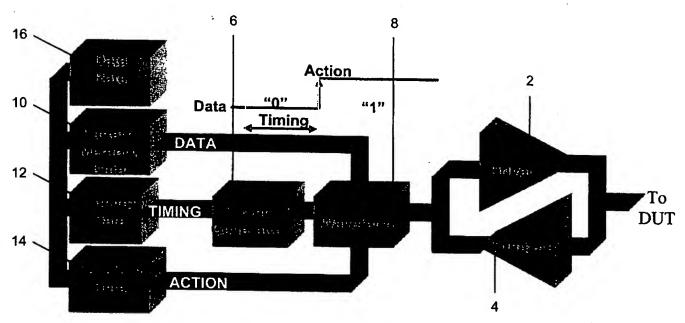
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Title: METHOD AND APPARATUS FOR TESTING INTEGRATED CIRCUITS
Appln. No: Not Yet Assigned
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Action = Pattern Data * Timing Data (Limited by TimeSet) * Waveset * Drive

Figure 1

Tide: METHOD AND APPARATUS FOR TESTING INTEGRATED CIRCUITS

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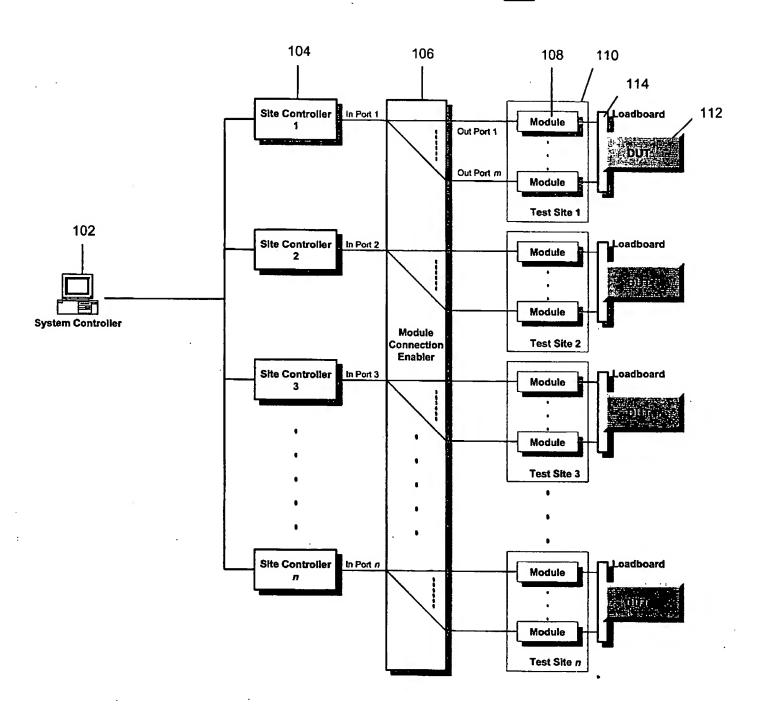


Figure 2

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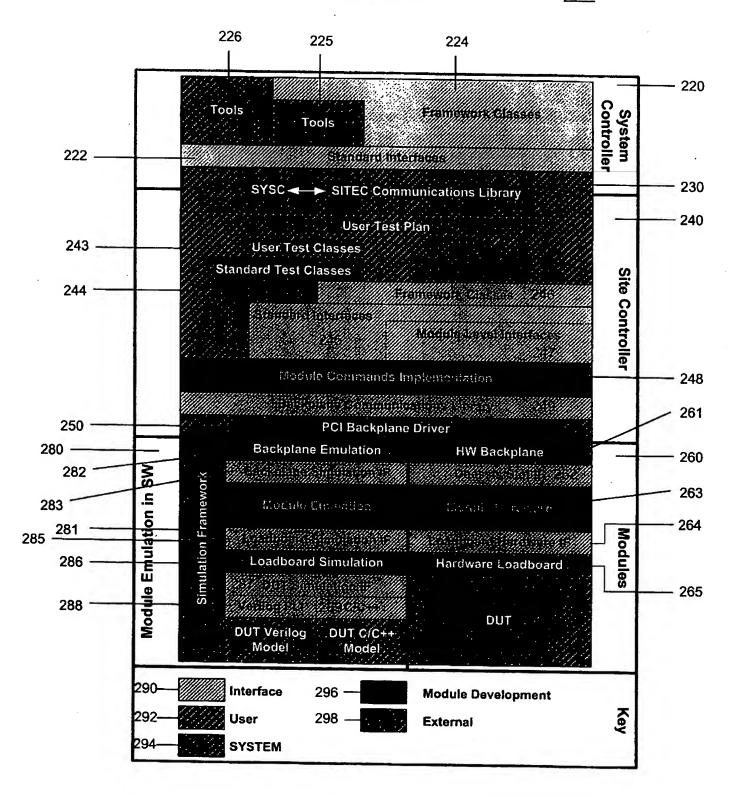


Figure 3

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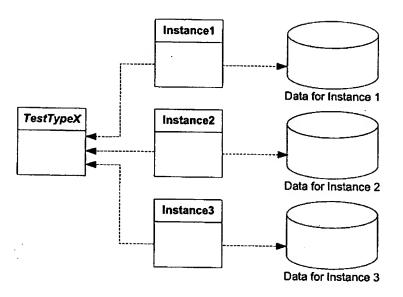


Figure 4

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Vendor A Implementation Vendor B implementation

Figure 5

Title: METHOD AND APPARATUS FOR TESTING

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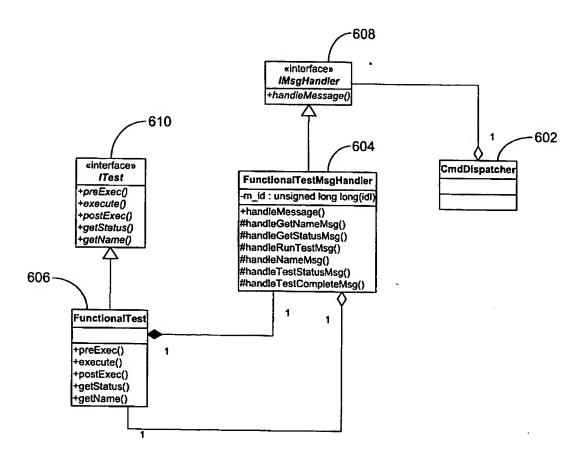


Figure 6

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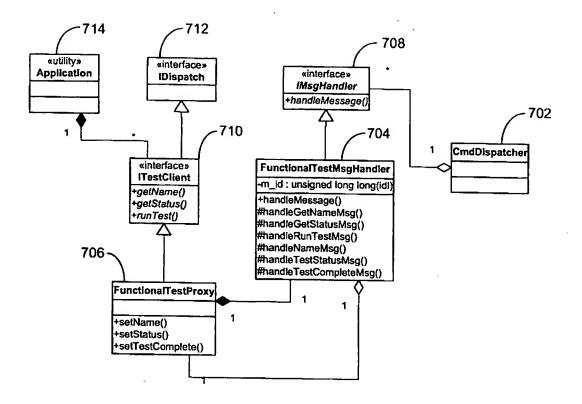


Figure 7

Title: METHOD AND APPARATUS FOR TESTING

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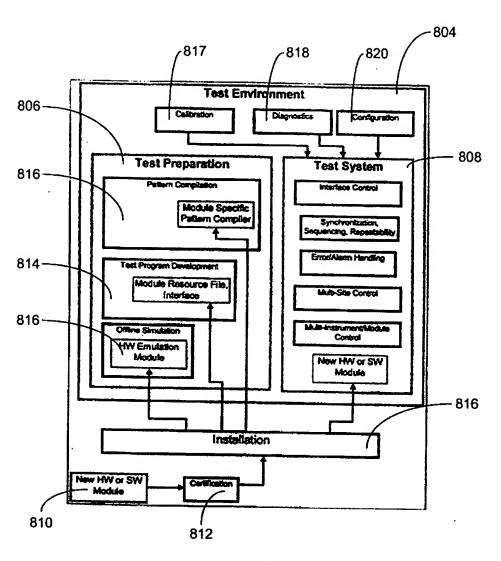


Figure 8